

## WEST Search History

DATE: Sunday, July 25, 2004

<b>Hide?</b>	<b>Set Name</b>	<b>Query</b>	<b>Hit Count</b>
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*DB=PGPB,USPT; PLUR=YES; OP=ADJ*

<input type="checkbox"/>	L19	L18 and second current path	9
<input type="checkbox"/>	L18	L17 and current path	54
<input type="checkbox"/>	L17	L16 and voltage	186
<input type="checkbox"/>	L16	L15 and reference cell	186
<input type="checkbox"/>	L15	L14 and drain and source	2943
<input type="checkbox"/>	L14	L13 and read	3597
<input type="checkbox"/>	L13	L12 and power supply	3973
<input type="checkbox"/>	L12	L11 and gate	6736
<input type="checkbox"/>	L11	L9 and MOS transistor	6878
<input type="checkbox"/>	L10	L9 and MOS tarnsistor	0
<input type="checkbox"/>	L9	L8 and sense amplifier	24355
<input type="checkbox"/>	L8	memory	633415

**VIET Q. NGUYEN**  
**PRIMARY EXAMINER**

*DB=USOC; PLUR=YES; OP=ADJ*

<input type="checkbox"/>	L7	L6 and transistor	80
<input type="checkbox"/>	L6	L5 and MOS	171
<input type="checkbox"/>	L5	L4 and sense amplifier	784
<input type="checkbox"/>	L4	memory	16453

*DB=PGPB,USPT; PLUR=YES; OP=ADJ*

<input type="checkbox"/>	L3	memory	633415
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*DB=USPT; PLUR=YES; OP=ADJ*

<input type="checkbox"/>	L2	6438038	5
<input type="checkbox"/>	L1	6674668	1

END OF SEARCH HISTORY